



#16/Amend C
(NES)
Q7061
7/14/03

SECOND AMENDMENT UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP 2816
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q65962

Katsuji KIMURA

Appln. No.: 09/940,472

Group Art Unit: 2816

Confirmation No.: 4891

Examiner: Minh T. NGUYEN

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL
AMPLIFIER CIRCUIT THEREFOR

BRIF 7/14/03

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ENTER FOR
SECOND AMENDMENT UNDER 37 C.F.R. § 1.116

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In further response to the Office Action dated January 27, 2003, and supplementing the
Amendment Under 37 C.F.R. § 1.116 filed May 16, 2003, please amend the above-identified
application as follows:

IN THE SPECIFICATION:

Please replace the formula at the top of page 21 with the following as a typographical error
has been detected: